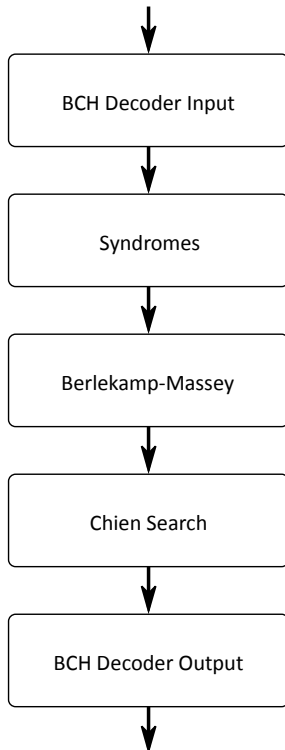


# BCH Encoder / Decoder IP Core

## IP Core Features

- Flexible code rate
- 500 MB/s throughput
- Pipelined design
- Decoding status
- Low latency



## Key Features

- Fully customizable to any standard, including NAND Flash and DVB
- Pipelined parallel Encoder and Decoder design
- High error correction performance
- Decoding status reporting
- Throughput up to 500 MBytes/s
- Code block length (N) up to 8191 bytes
- Payload length (K) up to 8190 bytes
- Parity length up to 1536 bits

## Applications

- NAND Flash memory
- DVB-S2, DVB-S2X, DVB-T2, DVB-C2
- Wireless modems
- Systems with the need of high-throughput FEC Codes

## Deliverables

- Encrypted Netlist or Complete RTL Source Code versions
- IP Core testbench scripts and design examples for evaluation boards
- Comprehensive integration guide
- Free 1 year technical support and maintenance updates

## Product Options

- IP customization and integration services available on request
- Royalty-free license without quantitative restrictions
- Off-the-shelf Xilinx and Analog Devices evaluation boards support